



12-11-06

PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

DEC 07 2006

## TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

7

Application Number

10/080,219

Filing Date

February 21, 2002

First Named Inventor

Mehrdad Abrishami

Art Unit

2664

Examiner Name

Andrew Chung Cheung Lee

Attorney Docket No.

01-688

## ENCLOSURE (check all that apply)

- ☐ Fee Transmittal Form  
☐ Fee Attached  
☐ Amendment/Reply  
☐ After Final  
☐ Affidavits/declaration(s)  
☐ Extension of Time Request  
☐ Express Abandonment Request  
☐ Information Disclosure Statement  
☐ Certified Copy of Priority Document(s)  
☐ Reply to Missing Parts/Incomplete Application  
☐ Reply to Missing Parts under 37 CFR 1.52 or 1.53

- ☒ Drawing(s)  
☐ Licensing-related Papers  
☐ Petition  
☐ Petition to Convert to a Provisional Application  
☐ Power of Attorney, Revocation Change of Correspondence Address  
☐ Terminal Disclaimer  
☐ Request for Refund  
☐ CD, Number of CD(s) \_\_\_\_\_  
☐ Landscape Table on CD

- ☐ After Allowance Communication to TC  
☐ Appeal Communication to Board of Appeals and Interferences  
☐ Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)  
☐ Proprietary  
☐ Status Letter

☒ Other Enclosure(s) (please identify below):

1. Returns address post card for PTO mailroom to date stamp.

Remarks

Payment of Issue Fee, Publication Fee and Submission of Formal Drawings

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name

LSI Logic Corporation

Signature

Printed Name

Henry Groth

Phone [+1] 408-433-4578

Date

12/6/2006

Reg. No.

39,696

## CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature

Typed or printed name

Manu Kashyap, Phone: [+1] 408-433-7475

Date

12/07/06

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Jianwei Bei



Serial No.: 10/080,219

Filed: 21-February-2002

For: Method for Eliminating Multiple Modulation and Dem

Group Art Unit: 2664

Examiner: Lee, Andrew Chung Cheung

Atty Docket: 01-688

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to:  
Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450,  
on the date below:

Manu Kashyap

12/07/06 *Manu Kashyap*

Date

Signature

COPY

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milipitas, CA 95035  
(408) 433-8000

Date: 12/6/2006

Respectfully submitted,

*Henry Groth*

Henry Groth

Reg. No.: 39,696

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Serial No.: 10/080,219

Filed: 21-February-2002

For: Method for Eliminating Multiple  
Modulation and Dem



Group Art Unit: 2664

Examiner: Lee, Andrew Chung Cheung

Atty Docket: 01-688

I hereby certify that this correspondence is being deposited with the U.S.  
Postal Service as First Class Mail in an envelope addressed to:  
Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450,  
on the date below:

12/07/06 Manu Kashyap  
Date Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milipitas, CA 95035  
(408) 433-8000

Date: 12/6/2006

Respectfully submitted,

Henry Groth  
Henry Groth

Reg. No.: 39,696